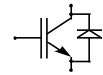


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Höchstzulässige Werte / Maximum rated values

Elektrische Eigenschaften / Electrical properties

Kollektor-Emitter-Sperrspannung collector-emitter voltage		V_{CES}	2500	V
Kollektor-Dauergleichstrom DC-collector current	$T_C = 80^\circ\text{C}$	$I_{C,nom.}$	500	A
	$T_C = 25^\circ\text{C}$	I_C	850	A
Periodischer Kollektor Spitzenstrom repetitive peak collector current	$t_p = 1\text{ ms}, T_C = 80^\circ\text{C}$	I_{CRM}	1000	A
Gesamt-Verlustleistung total power dissipation	$T_C=25^\circ\text{C}$, Transistor	P_{tot}	5,2	kW
Gate-Emitter-Spitzenspannung gate-emitter peak voltage		V_{GES}	+/- 20V	V
Dauergleichstrom DC forward current		I_F	500	A
Periodischer Spitzenstrom repetitive peak forw. current	$t_p = 1\text{ ms}$	I_{FRM}	1000	A
Grenzlastintegral der Diode I^2t - value, Diode	$V_R = 0\text{V}, t_p = 10\text{ms}, T_{vj} = 125^\circ\text{C}$	I^2t	100	kA^2s
Isolations-Prüfspannung insulation test voltage	RMS, $f = 50\text{ Hz}, t = 1\text{ min.}$	V_{ISOL}	5	kV

Charakteristische Werte / Characteristic values

Transistor / Transistor

			min.	typ.	max.	
Kollektor-Emitter Sättigungsspannung collector-emitter saturation voltage	$I_C = 500\text{A}, V_{GE} = 15\text{V}, T_{vj} = 25^\circ\text{C}$	$V_{CE\text{ sat}}$	-	3,0	3,6	V
	$I_C = 500\text{A}, V_{GE} = 15\text{V}, T_{vj} = 125^\circ\text{C}$		-	3,85	-	V
Gate-Schwellenspannung gate threshold voltage	$I_C = 40\text{mA}, V_{CE} = V_{GE}, T_{vj} = 25^\circ\text{C}$	$V_{GE(th)}$	4,3	5,3	6,3	V
Gateladung gate charge	$V_{GE} = -15\text{V} \dots +15\text{V}$	Q_G	-	8	-	μC
Eingangskapazität input capacitance	$f = 1\text{MHz}, T_{vj} = 25^\circ\text{C}, V_{CE} = 25\text{V}, V_{GE} = 0\text{V}$	C_{ies}	-	60	-	nF
Rückwirkungskapazität reverse transfer capacitance	$f = 1\text{MHz}, T_{vj} = 25^\circ\text{C}, V_{CE} = 25\text{V}, V_{GE} = 0\text{V}$	C_{res}	-	-	-	nF
Kollektor-Emitter Reststrom collector-emitter cut-off current	$V_{CE} = 2500\text{V}, V_{GE} = 0\text{V}, T_{vj} = 25^\circ\text{C}$	I_{CES}	-	0,1	-	mA
	$V_{CE} = 2500\text{V}, V_{GE} = 0\text{V}, T_{vj} = 125^\circ\text{C}$		-	20	-	mA
Gate-Emitter Reststrom gate-emitter leakage current	$V_{CE} = 0\text{V}, V_{GE} = 20\text{V}, T_{vj} = 25^\circ\text{C}$	I_{GES}	-	-	600	nA

prepared by: Oliver Schilling

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approved by: Chr. Lübke; 14.06.00

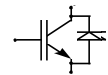
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Charakteristische Werte / Characteristic values

Transistor / Transistor

			min.	typ.	max.	
Einschaltverzögerungszeit (ind. Last) turn on delay time (inductive load)	$I_C = 500A, V_{CE} = 1200V$	$t_{d,on}$	-	0,85	-	μs
	$V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 25^\circ C,$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 125^\circ C$			0,85	-	μs
Anstiegszeit (induktive Last) rise time (inductive load)	$I_C = 500A, V_{CE} = 1200V$	t_r	-	0,2	-	μs
	$V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 25^\circ C,$ $V_{GE} = \pm 15V, R_G = 2,7\Omega, T_{vj} = 125^\circ C$			0,2	-	μs
Abschaltverzögerungszeit (ind. Last) turn off delay time (inductive load)	$I_C = 500A, V_{CE} = 1200V$	$t_{d,off}$	-	2,2	-	μs
	$V_{GE} = \pm 15V, R_G = 5,6\Omega, T_{vj} = 25^\circ C,$ $V_{GE} = \pm 15V, R_G = 5,6\Omega, T_{vj} = 125^\circ C$			2,2	-	μs
Fallzeit (induktive Last) fall time (inductive load)	$I_C = 500A, V_{CE} = 1200V$	t_f	-	0,2	-	μs
	$V_{GE} = \pm 15V, R_G = 5,6\Omega, T_{vj} = 25^\circ C,$ $V_{GE} = \pm 15V, R_G = 5,6\Omega, T_{vj} = 125^\circ C$			0,2	-	μs
Einschaltverlustenergie pro Puls turn-on energy loss per pulse	$I_C = 500A, V_{CE} = 1200V, V_{GE} = \pm 15V$ $R_G = 2,7\Omega, T_{vj} = 125^\circ C, L_S = 60nH$	E_{on}	-	650	-	mWs
Abschaltverlustenergie pro Puls turn-off energy loss per pulse	$I_C = 500A, V_{CE} = 1200V, V_{GE} = \pm 15V$ $R_G = 5,6\Omega, T_{vj} = 125^\circ C, L_S = 60nH$	E_{off}	-	500	-	mWs
Kurzschlußverhalten SC Data	$t_p \leq 10\mu sec, V_{GE} \leq 15V$ $T_{vj} \leq 125^\circ C, V_{CC} = 1200V, V_{CEmax} = V_{CES} - L_{sCE} \cdot di/dt$	I_{SC}	-	2000	-	A
Modulinduktivität stray inductance module		L_{sCE}	-	25	-	nH
Modulleitungswiderstand, Anschlüsse - Chip module lead resistance, terminals - chip		R_{CC+EE}	-	0,37	-	m Ω

Diode / Diode

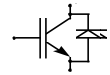
			min.	typ.	max.	
Durchlaßspannung forward voltage	$I_F = 500A, V_{GE} = 0V, T_{vj} = 25^\circ C$	V_F	-	2,2	2,8	V
	$I_F = 500A, V_{GE} = 0V, T_{vj} = 125^\circ C$			2,25	-	V
Rückstromspitze peak reverse recovery current	$I_F = 500A, -di_F/dt = 3000A/\mu s$	I_{RM}	-	530	-	A
	$V_R = 1200V, V_{GE} = -10V, T_{vj} = 25^\circ C$ $V_R = 1200V, V_{GE} = -10V, T_{vj} = 125^\circ C$			600	-	A
Sperrverzögerungsladung recovered charge	$I_F = 500A, -di_F/dt = 3000A/\mu s$	Q_r	-	270	-	μAs
	$V_R = 1200V, V_{GE} = -10V, T_{vj} = 25^\circ C$ $V_R = 1200V, V_{GE} = -10V, T_{vj} = 125^\circ C$			500	-	μAs
Abschaltenergie pro Puls reverse recovery energy	$I_F = 500A, -di_F/dt = 3000A/\mu s$	E_{rec}	-	200	-	mWs
	$V_R = 1200V, V_{GE} = -10V, T_{vj} = 25^\circ C$ $V_R = 1200V, V_{GE} = -10V, T_{vj} = 125^\circ C$			400	-	mWs

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Thermische Eigenschaften / Thermal properties

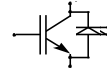
			min.	typ.	max.	
Innerer Wärmewiderstand thermal resistance, junction to case	Transistor / transistor, DC	R_{thJC}	-	-	0,024	K/W
	Diode/Diode, DC		-	-	0,048	K/W
Übergangs-Wärmewiderstand thermal resistance, case to heatsink	pro Modul / per Module $d_{Paste} \leq 100\mu\text{m} / d_{grease} \leq 100\mu\text{m}$	R_{thCK}	-	0,006	-	K/W
Höchstzulässige Sperrschichttemperatur maximum junction temperature		T_{vj}	-	-	150	°C
Betriebstemperatur operation temperature		T_{op}	-40	-	125	°C
Lagertemperatur storage temperature		T_{stg}	-40	-	125	°C

Mechanische Eigenschaften / Mechanical properties

Gehäuse, siehe Anlage case, see appendix					
Innere Isolation internal insulation				AlN	
Kriechstrecke creepage distance				32	mm
Luftstrecke clearance				19,1	mm
CTI comperative tracking index				>400	
Anzugsdrehmoment f. mech. Befestigung mounting torque		M1		5	Nm
Anzugsdrehmoment f. elektr. Anschlüsse terminal connection torque	terminals M4	M2		2	Nm
	terminals M8			8 - 10	Nm
Gewicht weight		G		1000	g

Mit dieser technischen Information werden Halbleiterbauelemente spezifiziert, jedoch keine Eigenschaften zugesichert. Sie gilt in Verbindung mit den zugehörigen Technischen Erläuterungen.

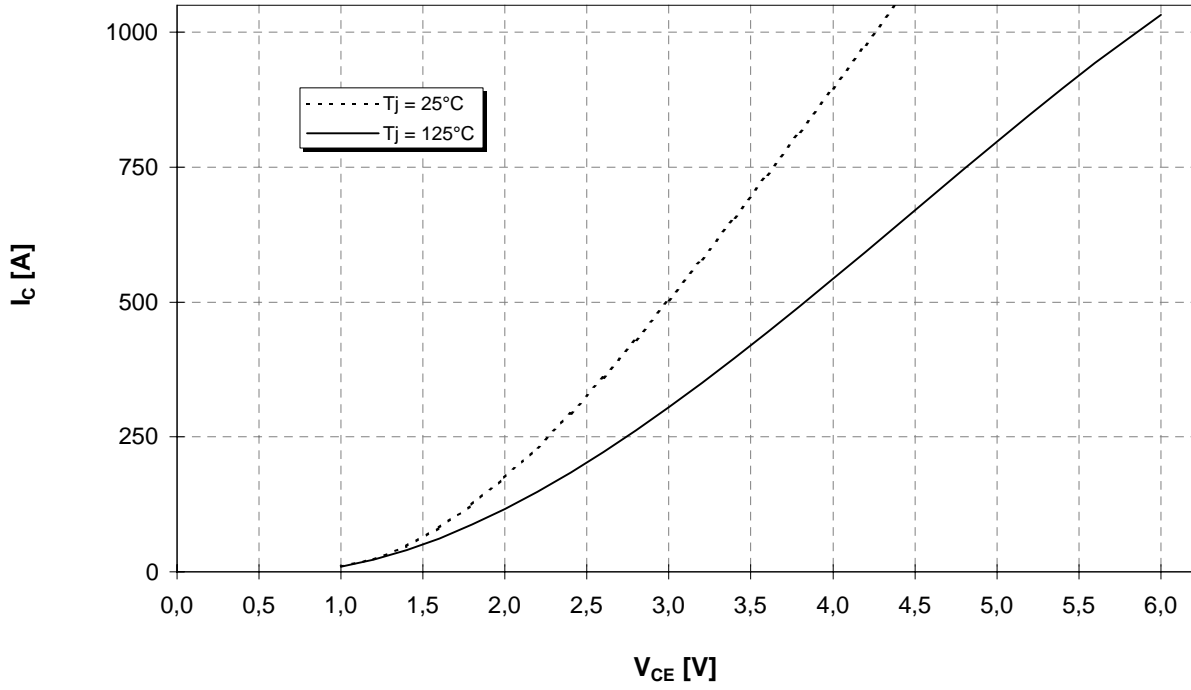
This technical information specifies semiconductor devices but promises no characteristics. It is valid in combination with the belonging technical notes.



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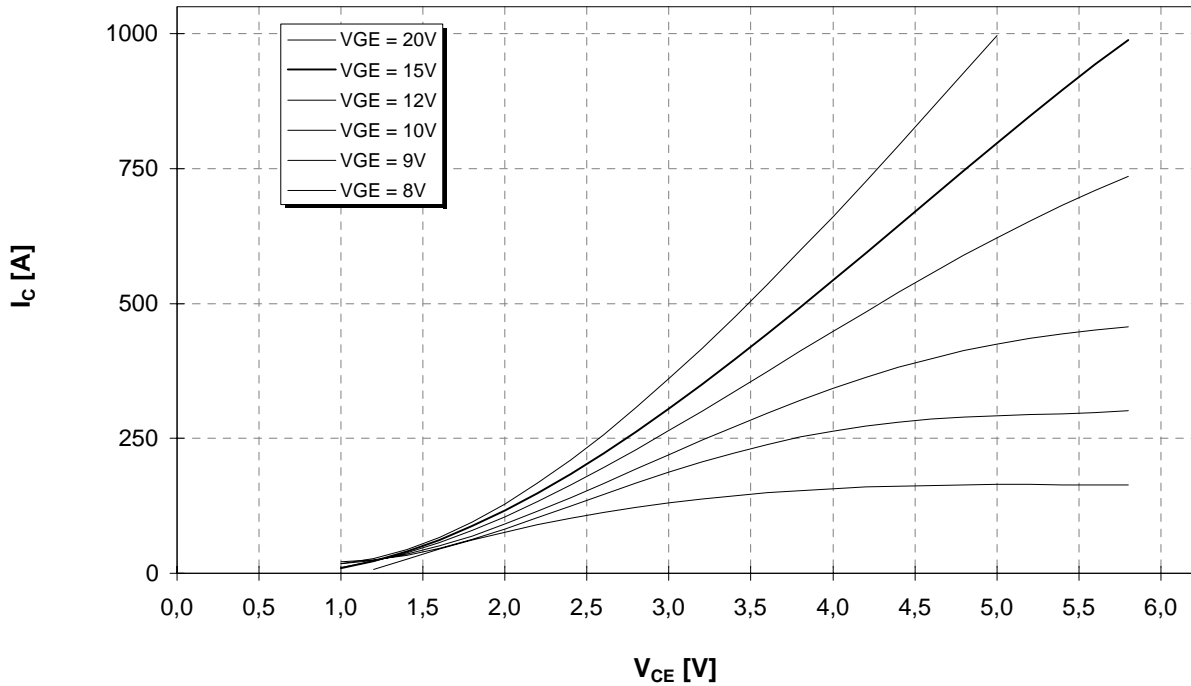
Ausgangskennlinie (typisch)
Output characteristic (typical)

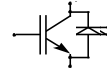
$I_C = f(V_{CE})$
 $V_{GE} = 15V$



Ausgangskennlinienfeld (typisch)
Output characteristic (typical)

$I_C = f(V_{CE})$
 $T_{vj} = 125^\circ C$

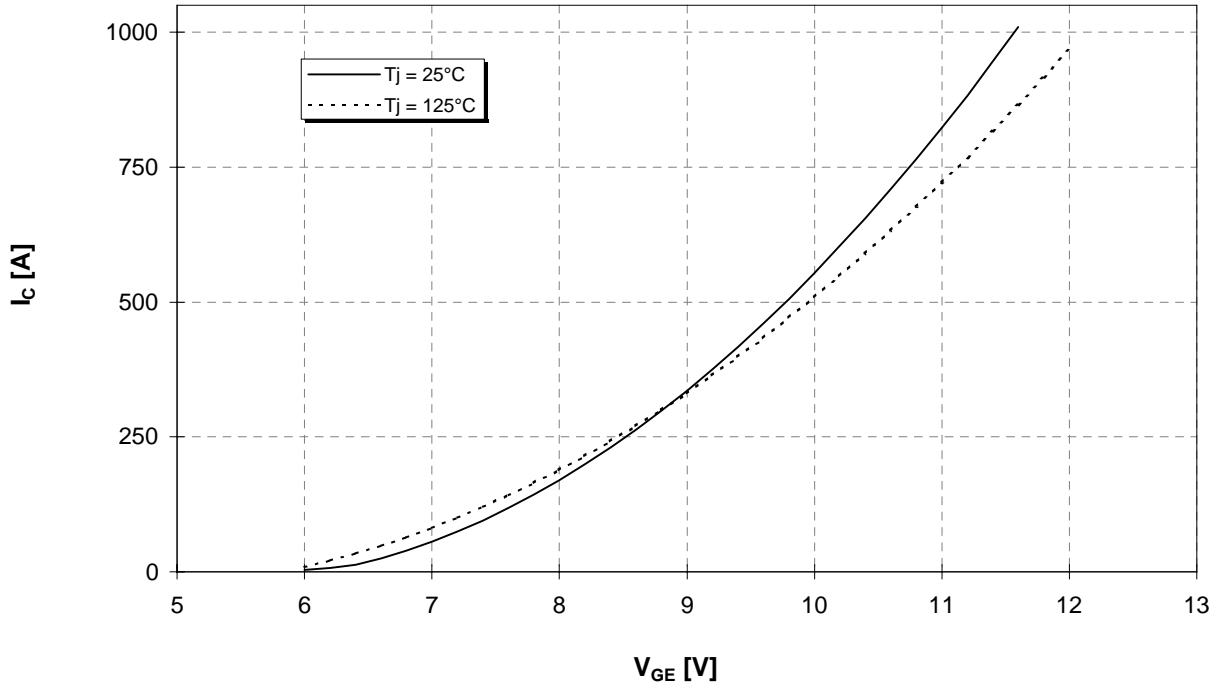




vorläufige Daten
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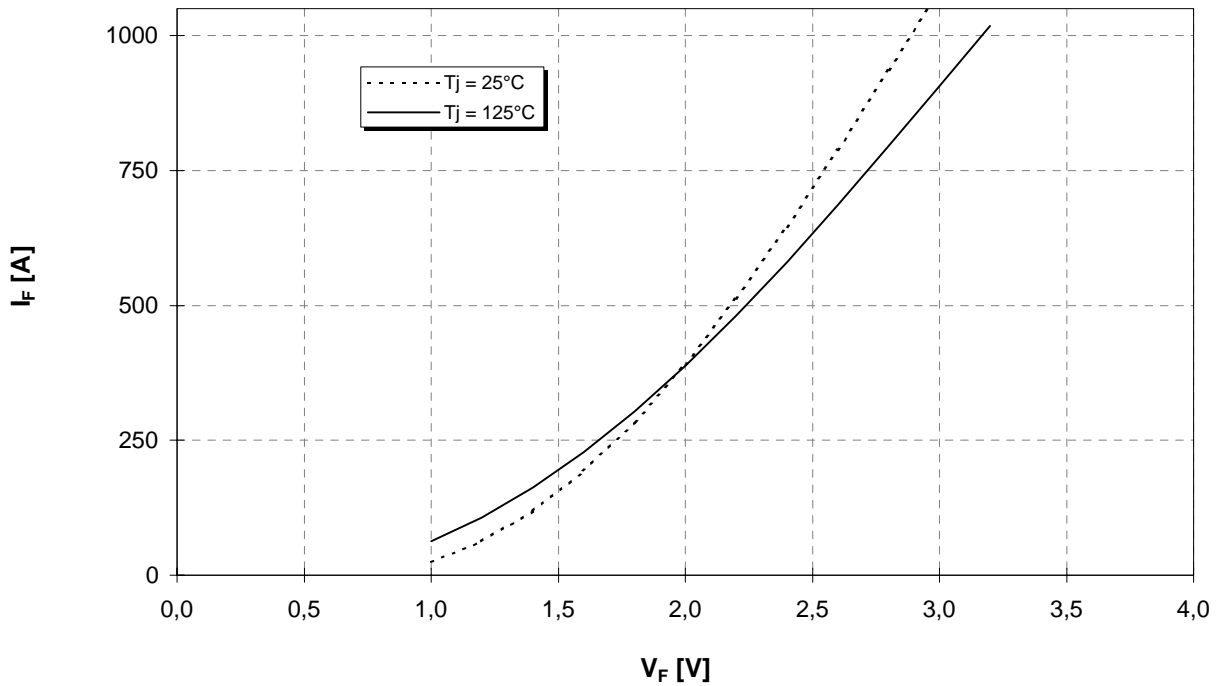
Übertragungscharakteristik (typisch)
Transfer characteristic (typical)

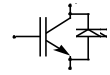
$I_C = f(V_{GE})$
 $V_{CE} = 10V$



Durchlaßkennlinie der Inversdiode (typisch)
Forward characteristic of inverse diode (typical)

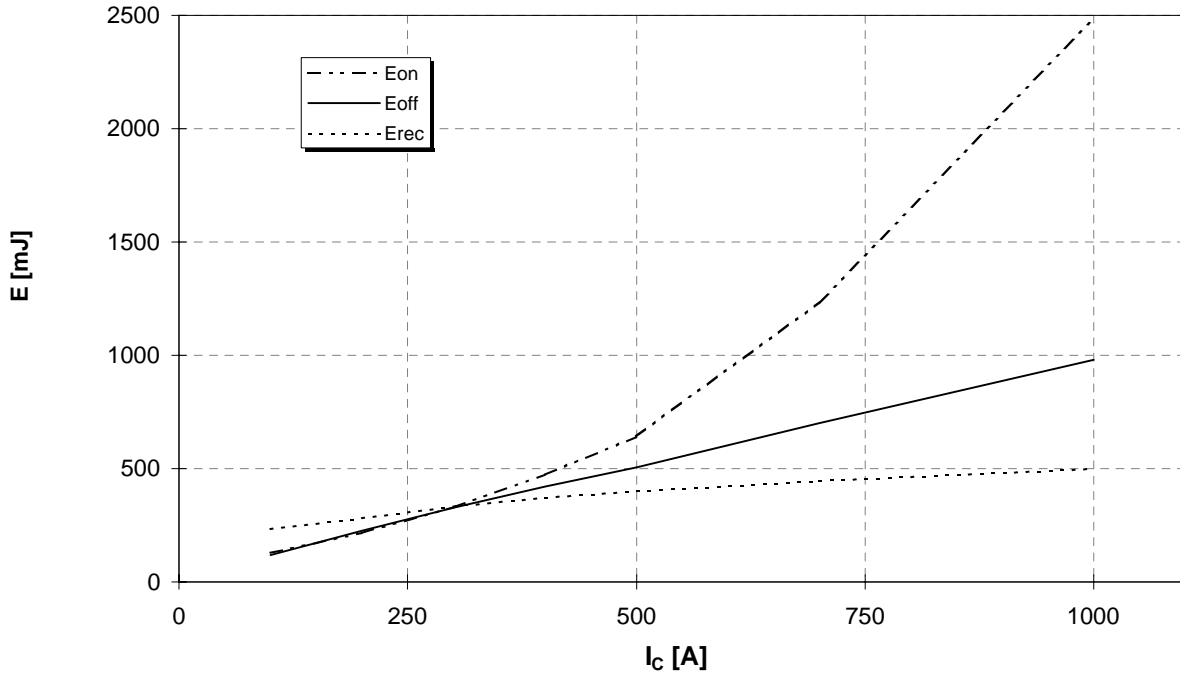
$I_F = f(V_F)$



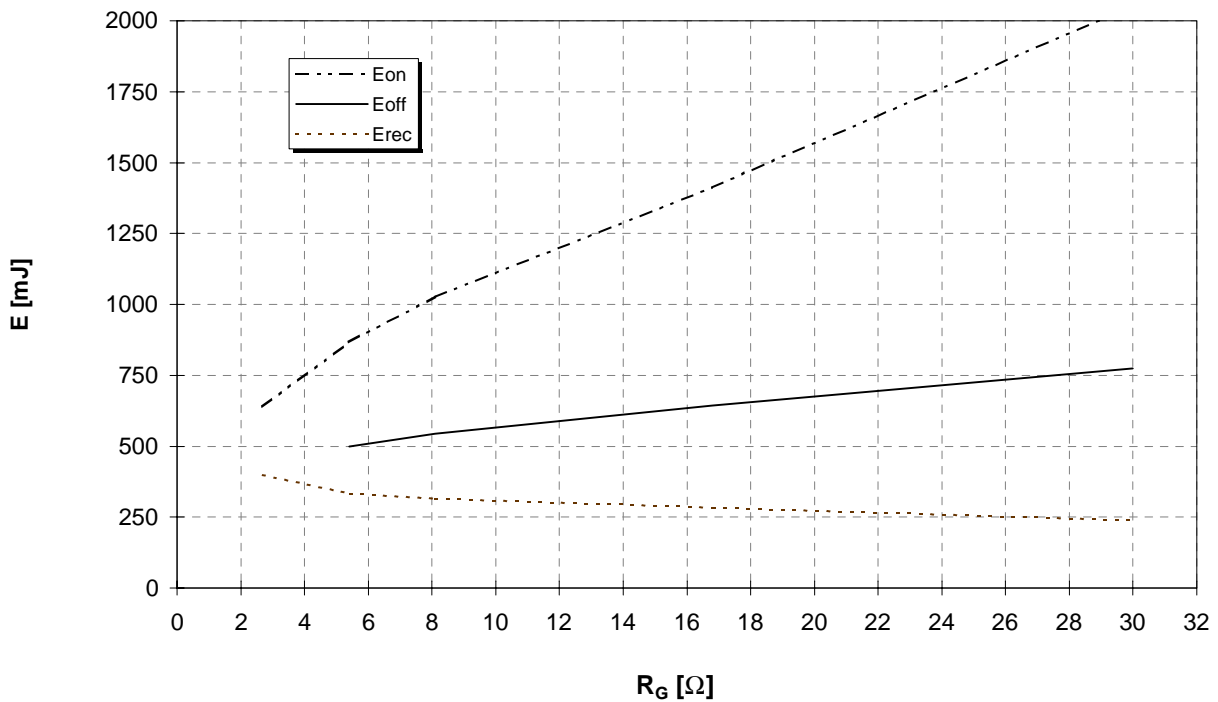


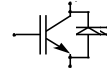
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Schaltverluste (typisch) $E_{on} = f(I_C)$, $E_{off} = f(I_C)$, $E_{rec} = f(I_C)$
 Switching losses (typical) $R_{gon}=2,7\Omega$, $R_{goff}=5,6\Omega$, $V_{GE}=\pm 15V$, $V_{CE} = 1200V$, $T_j = 125^\circ C$, $L_S = 60nH$



Schaltverluste (typisch) $E_{on} = f(R_G)$, $E_{off} = f(R_G)$, $E_{rec} = f(R_G)$
 Switching losses (typical) $I_C = 500A$, $V_{CE} = 1200V$, $V_{GE}=\pm 15V$, $T_j = 125^\circ C$, $L_S = 60nH$

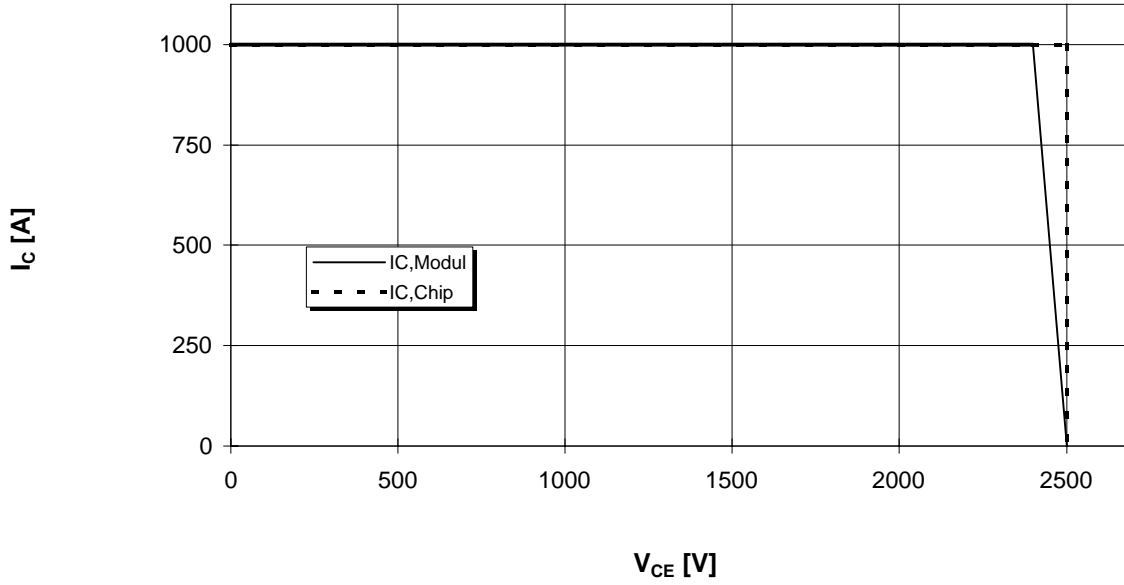




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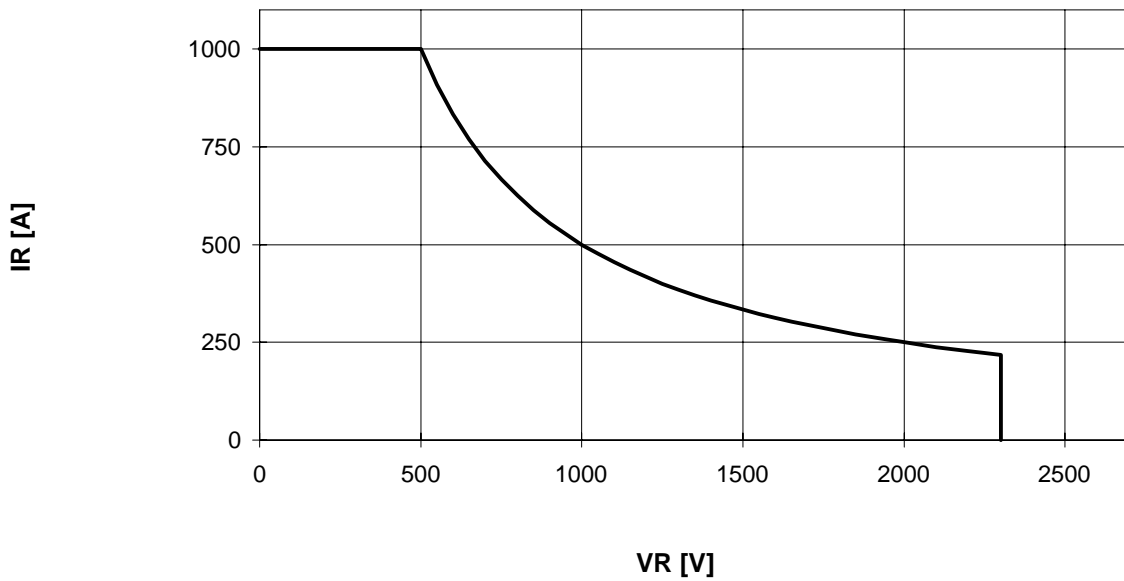
Sicherer Arbeitsbereich (RBSOA)

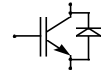
Reverse bias safe operation area (RBSOA) $R_g = 5,4 \text{ Ohm}$, $T_{vj} = 125^\circ\text{C}$



Sicherer Arbeitsbereich Diode (SOA)
safe operation area Diode (SOA)

$T_{vj} = 125^\circ\text{C}$

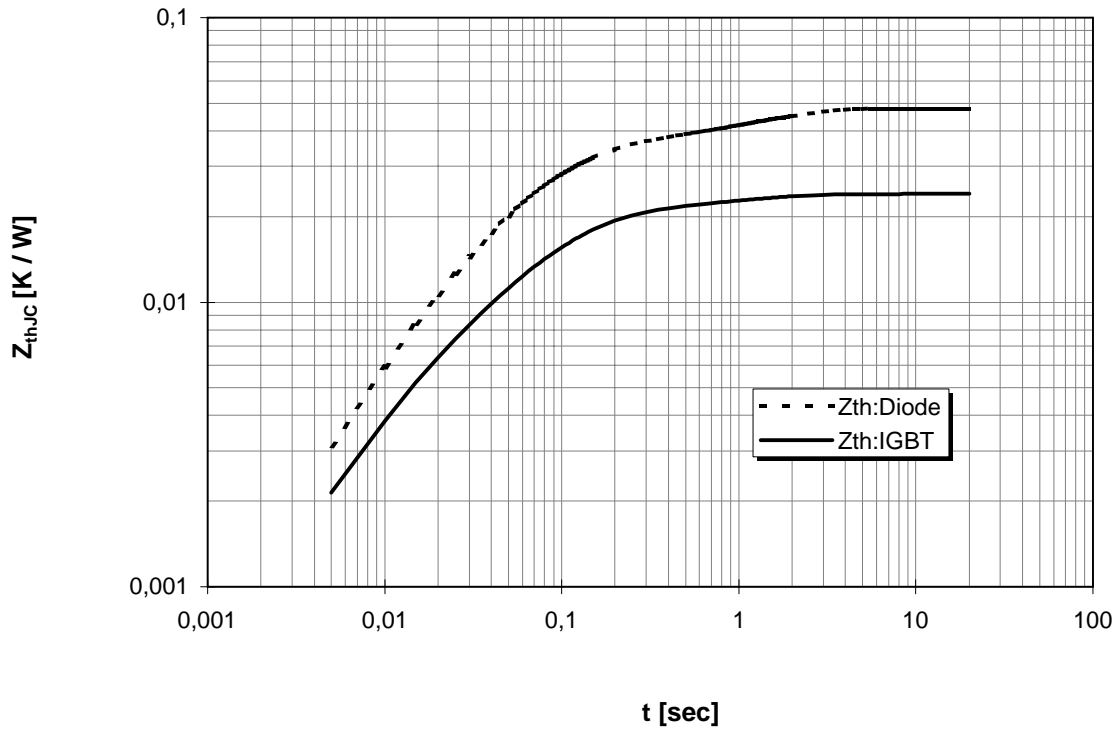




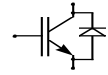
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Transienter Wärmewiderstand
Transient thermal impedance

$$Z_{thJC} = f(t)$$

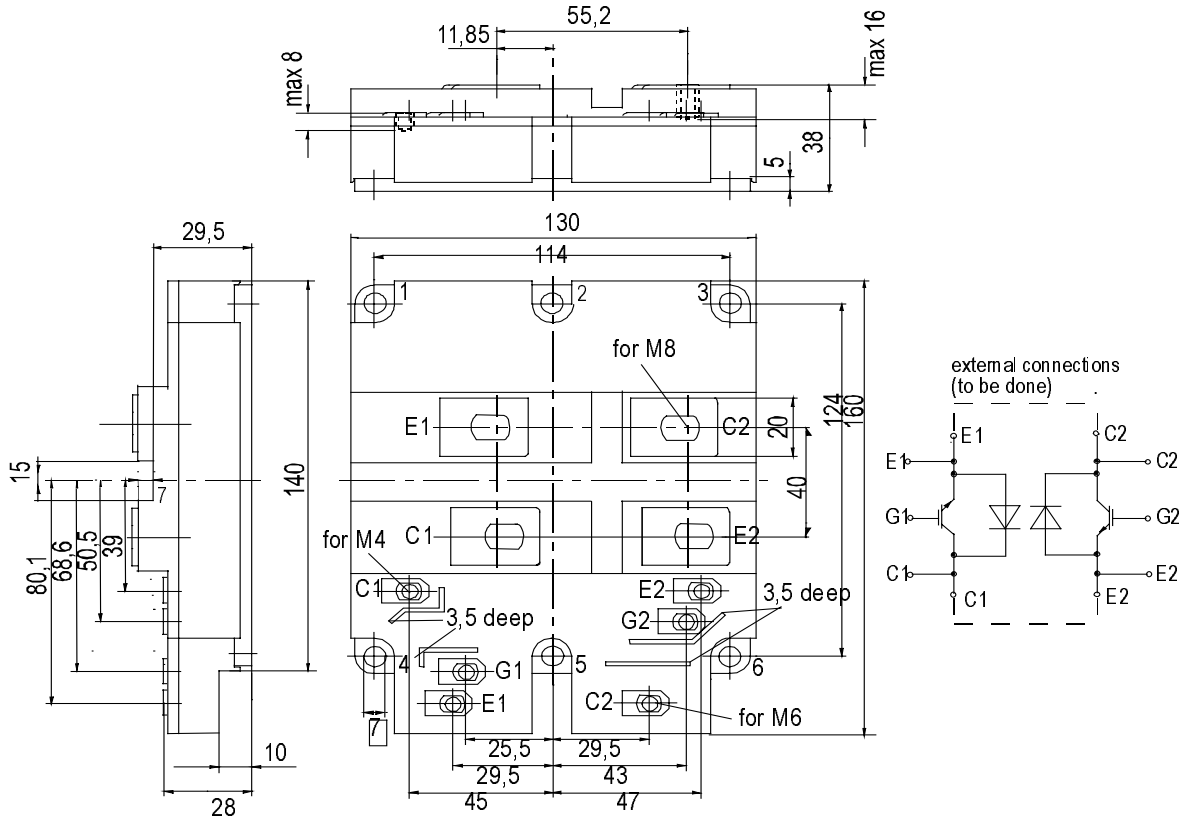


i	1	2	3	4
r_i [K/kW] : IGBT	2,7	6,6	11,1	3,6
τ_i [sec] : IGBT	0,011	0,052	0,103	0,95
r_i [K/kW] : Diode	6,3	15	13,5	13,2
τ_i [sec] : Diode	0,025	0,056	0,1	1,31



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Äußere Abmessungen und Schaltbild /
external dimensions and circuit diagram



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