

WESTCODE SEMICONDUCTORS

Series
SWxxC/DXC32C

Capsule Rectifier Diode

Consists of a diffused silicon element mounted in an hermetic ceramic cold welded capsule. Available in industry standard and thin housings.

Ratings	Unless otherwise stated $T_j = 190^\circ\text{C}$	Maximum Limits							Units
	Voltage Codes	02	04	06	08	10	12	14	
V_{RRM}	Repetitive peak reverse voltage.	200	400	600	800	1000	1200	1400	V
	Non-repetitive peak reverse voltage.	300	500	700	900	1100	1300	1500	V

$I_{F(AV)}$	Average forward current	Half sine wave	55°C heatsink temperature (double side cooled)	8410	A
			100°C heatsink temperature (single side cooled)	3935	A
$I_{F(RMS)}$	R.M.S forward current	25°C heatsink temperature, double side cooled	15025	A	
I_F	Continuous forward current	25°C heatsink temperature, double side cooled	12920	A	
$I_{FSM(1)}$	Peak one-cycle surge	10ms duration, 60% V_{RRM} re-applied	72.0	KA	
$I_{FSM(2)}$	Peak one-cycle surge	10ms duration, $V_R \leq 10$ volts	79.2	KA	
$I^2t_{(2)}$	Maximum permissible surge energy	10ms duration, $V_R \leq 10$ volts	31.4×10^{-6}	A^2s	
		3ms duration, $V_R \leq 10$ volts	22.5×10^{-6}	A^2s	
T_j	Operating temperature range		-55 to +190	$^\circ\text{C}$	
T_{stg}	Storage temperature range		-55 to +200	$^\circ\text{C}$	

Characteristics Unless otherwise indicated $T_j = 190^\circ\text{C}$	
V_{FM}	Peak forward voltage
V_O	Forward conduction threshold voltage
r	Forward conduction slope resistance
I_{RRM}	Repetitive peak reverse current
$R_{th(j-hs)}$	Thermal resistance, junction to heat sink.
	$I_F = 6800$ A
	At V_{RRM}
	Double side cooled
	Single side cooled

Ordering Information (Please quote device code as explained below - 10 digits)

S	W	• •	• X C	3 2 C
Fixed type code	Voltage Code (see ratings)	CXC - Thick Housing DXC - Thin Housing		Fixed Type Code

Typical code : SW10CXC32C, 1000 V_{RRM}

Details of a full range of capsule mounting clamps are available - ask for brochure.

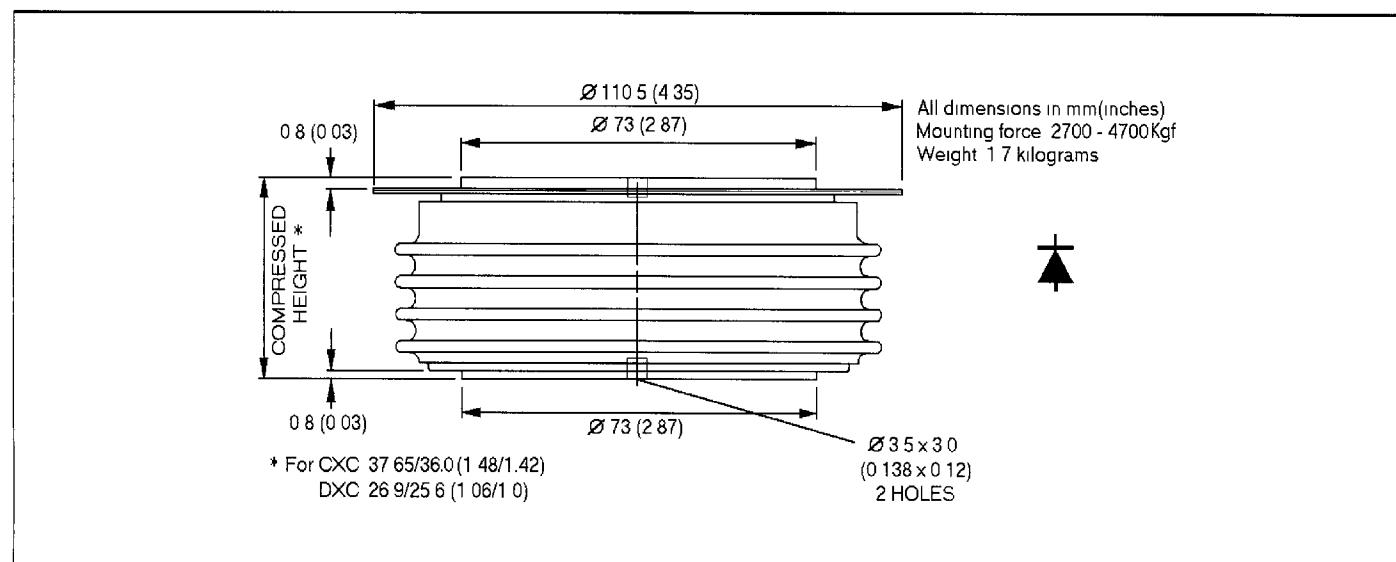


Figure 1. Dissipation/Sink Temperature v. Mean Forward Current.

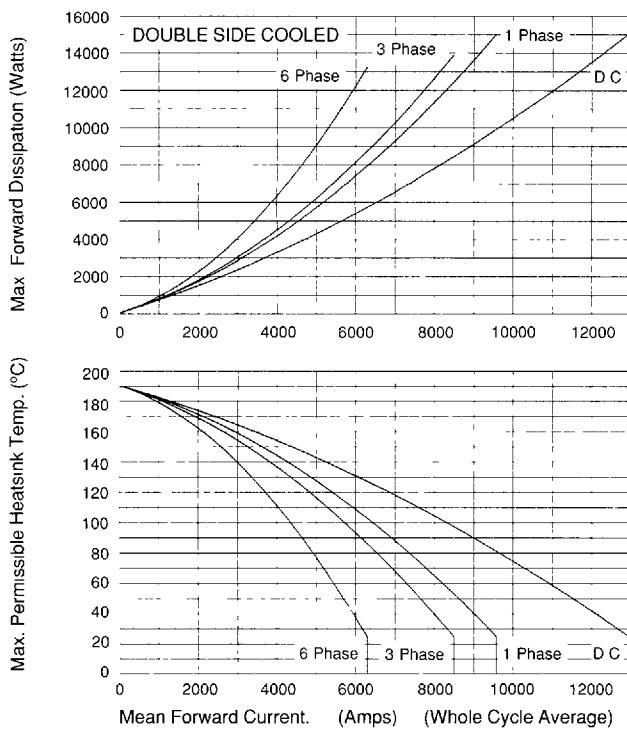
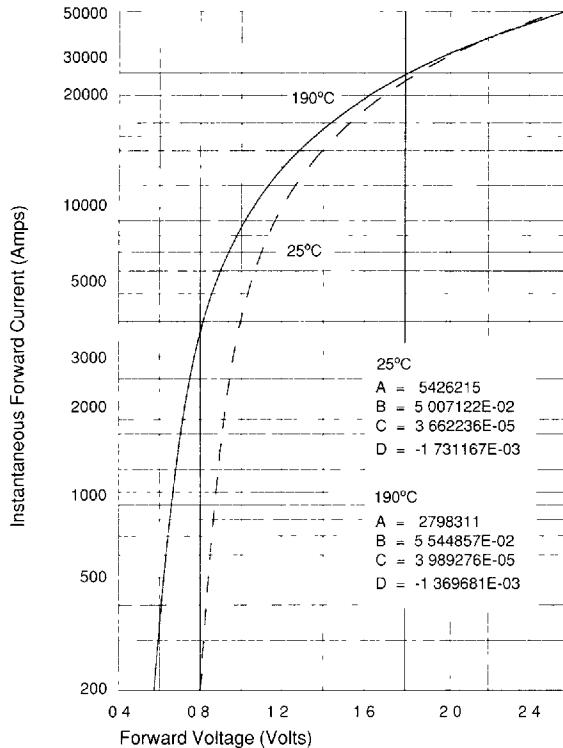


Figure 3. Limit Forward Characteristic at 190°C.



Forward volt-drop calculation:
 $V_F = A + B \ln I_F + CI_F + D/\sqrt{I_F}$

Figure 2. Dissipation/Sink Temperature v. Mean Forward Current.

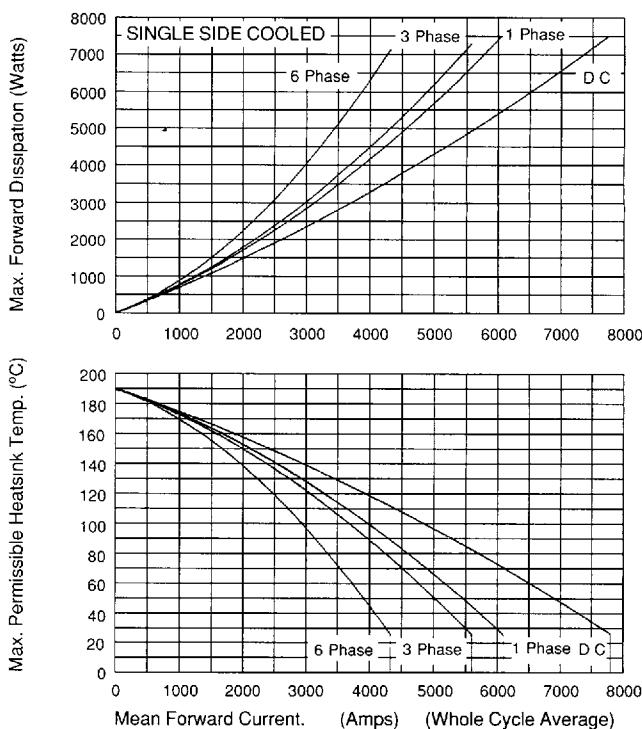


Figure 4. Junction to Sink Transient Thermal Impedance.

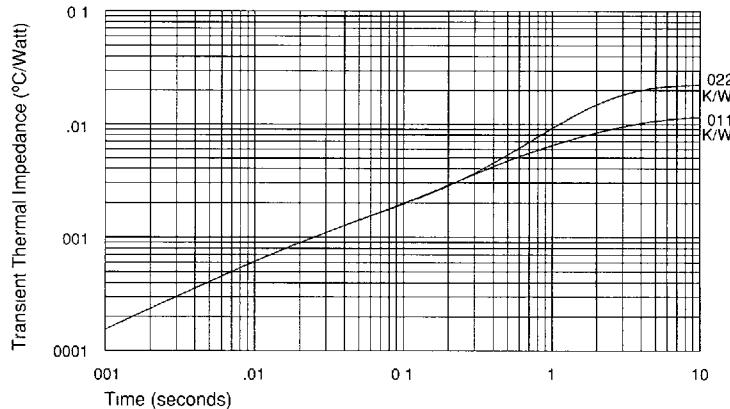
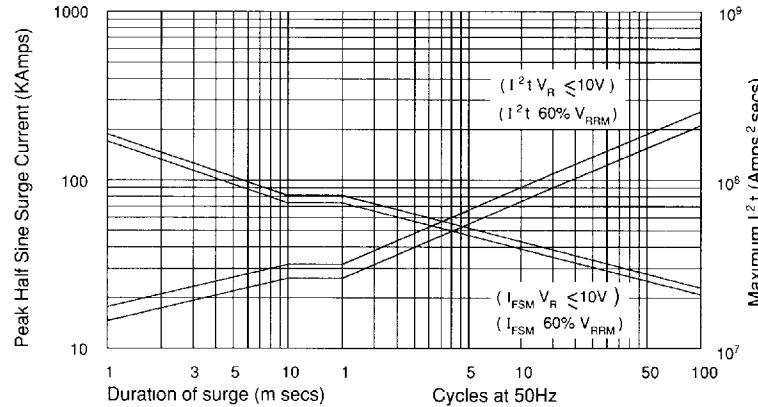


Figure 5. Non-Repetitive Surge Current at Initial Junction Temperature 190°C.



In the interest of product improvement, Westcode reserves the right to change specifications at any time without notice © Westcode Semiconductors Ltd



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